



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CONFIRMATION NO.:

ART UNIT:

**EXAMINER:** 

3811

2611

Sudhanshu C. Pathak

IN RE THE

APPLICATION OF:

Mark Anders, et al.

APPLICATION NO.:

10/035,574

FILED:

October 18, 2001

TITLE:

**TRANSITION** 

**ENCODED DYNAMIC** 

**BUS CIRCUIT** 

ASSIGNEE:

INTEL CORPORATION

## COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

MAIL STOP ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Notice of Allowability filed September 1, 2006, and more particularly in response to the Examiner's Statement of Reasons for Allowance ("RFA") attached as part of the Notice of Allowability, Applicant respectfully submits these Comments on Statement of Reasons for Allowance.

I, In Chung	, hereby certify that this correspondence is being deposited with the	
United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner		
for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on <u>December 1, 2006</u> .		
	$\mathcal{M}_{\alpha}$	
	(Signature of person mailing correspondence)	

The RFA might possibly serve to place unwarranted interpretations on the claims.

The RFA states:

Claims 21-40 and re-numbered as claims 1-20 respectively are allowable over the prior art of record because the cited references do not contain the specified limitation of an apparatus comprising [the entire body of claim 21].

Applicant notes that Applicant distinguished independent claims 21, 35, and 38 over prior art in the Response to Office Action mailed July 10, 2006. Applicant respectfully submits that the recitation of <u>all</u> features of claim 21 in the RFA should not serve to place unwarranted interpretations on claim 21 or any other claims.

Applicant also notes that independent claim 35 is a method claim and not an apparatus claim. Accordingly, reference to apparatus claim 21 in the RFA should not serve to place unwarranted interpretations on claim 35.

The RFA also refers to "Fig. 2, elements 202, 206 -- feedback to the input of second CMOS domino gate".

Applicant respectfully submits that such features refer to one embodiment that may be covered by Applicant's claims. Applicant respectfully submits that reference to such features in the RFA should not serve to place unwarranted interpretations on the claims.

As the RFA might possibly serve to place unwarranted interpretations on the claims, as noted by the above exemplary discrepancies, Applicant respectfully submits that the claims are to be construed in accordance with their express language.

Respectfully submitted,

Date: December 1, 2006

/Matthew C. Fagan, Reg. No. 37,542/

Matthew C. Fagan Registration No. 37,542 Telephone: (512) 732-3936

Facsimile: (512) 732-3912 (please call first)

c/o Blakely, Sokoloff, Taylor & Zafman, L.L.P. 12400 Wilshire Boulevard, Seventh Floor Los Angeles, California 90025

Telephone: (503) 439-8778 Telephone: (310) 207-3800

PE Ajto Docket #42390.P11426		
O IN THE UNITED	STATES PATENT	AND TRADEMARK OFFICE
RE Application of: Mark Anders, e	t al.	) )
Serial No.: 10/035,574		) Art Unit: 2611
Filing Date: October 18, 200	01	Examiner: Sudhanshu, C. Pathak
For: TRANSITION ENCODED DYNAMIC  BUS CIRCUIT  )		
Assistant Commissioner for F Washington, D.C. 20231 Attn: Official Draftsperson	Patents	)
<u>su</u>	IBMISSION OF FORM	IAL DRAWINGS
Dear Sir:		
In response to the Not	cice of Allowability	mailed 9/1/2006 enclosed herewith for
filing in the above-referenced	l patent applicatior	are formal drawings including 2 sheets
and 3 Figures. The drawing	informalities have	been noted and the appropriate
corrections are incorporated	herein.	
	Respectfully	submitted,
	BLAKELY, S	OKOLOFF, TAYLOR & ZAFMAN LLP
Date: December 1, 2006	Edwin H. Tay Reg. No. 25,	
12400 Wilshire Blvd., 7th FL Los Angeles, CA 90025-1026 (408) 720-8300		
I hereby certify that this correspondence is	FIRST CLASS CERTIFICA s being deposited with the U	Inited States Postal Service as first class mail with sufficient
postage in an envelope address	ed to: ASSISTANT COMMISS	SIONER FOR PATENTS, WASHINGTON, D.C. 20231.
In	Chung	Date